

**An Intellectual  
Property  
Toolkit for  
Scientists and  
Engineers**



**SF STATE**

***Eric Janofsky &  
Ron Powell***

Novellus Systems Inc.

**Sept 19, 2011**

**1.10-2pm**

**Thornton Hall  
327**

***A reception to  
follow from  
2.15-3pm in  
Science 256***

***Synopsis:***

Intellectual property (IP) refers to a product of the mind that can be legally protected -- e.g. the design of an improved robotic handler or a new method for depositing a thin film used in microelectronics. Since the commercial success of new technology often depends on its innovative aspects, every engineer and scientist should have a basic understanding of IP. This seminar will provide you with the essential tools to produce, protect and profit from your ideas -- an "IP toolkit". You will learn about the fundamentals of patents and other forms of IP, the most relevant points of IP law, and strategies for developing strong patent applications. Concepts will be illustrated with real-life case studies.

***Speaker Bios:***

**Eric Janofsky** is currently the Vice President and Associate General Counsel at Novellus Systems. He has held similar positions previously at Marvell Semiconductor Inc. and Epson Research and Development. Eric is a patent attorney and engineer. He received his JD from the Saint John's University School of Law, MS in management from Polytechnic Institute of New York and his BS and ME in electric power engineering from Rensselaer Polytechnic Institute. He has 13 years of engineering experience and has been practicing law since 1984.

**Ron Powell** received his PhD in physics from Stanford University and worked for many years at Varian Associates in Palo Alto as Director of the Varian Research Center. In 1997 he joined Novellus in San Jose as Director of Thin Film Research, and in 2003 was named as the first Novellus Fellow. Ron currently serves Novellus as a Patent Executive responsible for improving the quality and strategic focus of the company's intellectual property, supervising the preparation and prosecution of patent applications, and educating inventors about the patent process.

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