SFSU School of Engineering Seminar

“Architecting Future Advanced Technologies”

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Synopsis: Technology scaling has fueled the continued growth of the semiconductor industry. In the future, advanced technology landscape will drive many disruptive innovations and transitions. It is critical to establish strong capabilities to make early and comprehensive assessment of the implications of technology choices and options. A systematic approach is required to lead the technology direction and to cope with the challenges of the advanced technology nodes considering “application-informed” technology features. A solid starting point is to ensure the critical linkage in technology and design co-optimization is realized for the benefit of the future customer applications. Cost effective advanced technology platforms will be architected with the right fundamental technology components to assure “first time right” technology definition across a broad semiconductor application space covering computing, graphics, gaming, wireless, communication, and consumer electronics.

Speaker Bio: Ali received a Ph.D. from Purdue University and has over 15 years of industry experience. Ali is currently a R&D Fellow and Director of Advanced Technology Architecture at GLOBALFOUNDRIES defining 14nm technology architecture and platform to enable launching a variety of SoC products for a very broad range of systems and applications. Previously, Ali worked at TSMC as a Director of Design and Technology Platforms defining and leading device and circuit co-optimization for 20nm platform. Prior to TSMC, Ali worked at Intel Corporation in various capacities, most recently as a principal research scientist at Intel Labs in low-power/high-performance circuit techniques and transistor device structures for future generations of microprocessors. Ali has 40+ patents, 40+ papers, has received best-paper awards, and has received the prestigious Intel Achievement Award (IAA). Ali has served on the TPC committees of ISSCC, A-SSCC, ISLPED, ISQED and was the general chair of ISLPED.

Refreshments will be served – come join us!

For inquiries, please contact Dr. Kwok Siong Teh at ksteh@sfsu.edu.