

SFSU School of Engineering Seminar

“Microprocessor: how did we get here and where do we go?”

T. M. Mak

Sort/Test Technology Development Group
Intel Corporation



Monday, April 11

1:10 - 2 pm

SCI 256

Synopsis: *Microprocessors are ubiquitous in our everyday life since its invention 40 years ago. It has grown from 2300 transistors to over 2 billion transistors today. Its growth and maturation has been a long, winding road. We will take a look at this history and ponders what will come next.*

Speaker Bio: TM Mak is a Technologist with Intel Corporation’s Sort/Test Technology Development Group, working on test methodology development. He has been with Intel for over 27 years and has worked on a variety of areas including test development, product engineering, design automation and design for test. He mentored MARCO/FCRP (Focus Center Research Program) research for 5 years. He twice (1997 and 2004) received the SRC Outstanding Industrial Mentor Award. His current research interest ranges from defect based testing, fault effects as a result of nanometer technology, circuit level and physical design test issues, IO interface, memory and analog testing, fault tolerant and on-line testing. He received the best paper award in 2004 International Test Conference and a best panel award from 2004 VTS. He currently holds 14 patents with 1 more pending. He is a Senior Member of IEEE and a graduate from Hong Kong Polytechnic University.

Refreshments will be served – come join us!

For inquiries, please contact Dr. Kwok Siong Teh at ksteh@sfsu.edu.