Nano Electronics & Computing Research Laboratory

School of Engineering
San Francisco State University

SF STATE

NeCRL

Synopsys Charles Babbage Grant and Research Lab Opening Ceremony
Friday, Nov. 5th, 2010
1:30pm – 4:30pm

Keynote Talk and Presentations	1:30 pm - 3:30 pm Room: SCI 256
Lab Opening Ceremony and Reception	3:30 – 4:30pm Room: SCI 110

Keynote speaker: Don Chan (Vice President, CAE, Synopsys Inc.)
Keynote title: "Evolution of Design"
Abstract: This presentation provides an overview of how EDA (Electronic Design Automation) has helped the electronics industry in chip designs and how it has kept up with Moore's Law in addressing the technology advancement

Keynote is followed by award announcement and student presentations on use of Synopsys Electronic Design Automation tools

Speaker's Bio: Don is the Vice President of the Corporate Applications Engineering (CAE) organization in the Implementation Business Group at Synopsys where he started working in December 1992. He currently manages an organization responsible for working with R&D and Marketing in product definition, development, and release of products in the Implementation Business Group. The CAE organization also provides design and tools' methodology support to design teams in the electronics industry. Don has a successful track record in deploying IG products and supporting customers. He has more than 24 years of experience in IC design and EDA. His prior experience includes 7 years at Fujitsu Microelectronics and Spectrum Software. He holds a MSEE Degree from Santa Clara University and BSE Degree from San Francisco State University.

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