ASIC Design Lab
VLSI Research Lab

Hamid Mahmoodi
and teaching, lab, and research assistants
Lab Environment

100Mbps network

VLSI Research Lab (Prof. Mahmoodi’s Research Lab)

ASIC Design Lab (Used by related undergrad and grad courses)
Timeline

Fall 2005
- Digital VLSI Design course offered (grad level)
- Linux workstations already in place for the ASIC lab
- Linux workstations are purchased for the VLSI research lab
- Installation of Cadance and Synopsys tools and MOSIS technology files and setup of design flow

Spring 2006
- Equipment and CAD tools ready to be used
- Advanced VLSI Design course offered (grad level)
- VLSI Research Lab will open

Fall 2006
- ASIC Design course offered (undergraduates level)